

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		09/751,194	CATHEY ET AL.	
Examiner		Tri H. Phan	Art Unit	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Brown et al., 'An IRAM-Based Architecture for a Single-Chip ATM Switch', TR-07-97, Center for Research in Computing Technology, Harvard University, Cambridge, Massachusetts, pp 1-32.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.